



SOT8023-1

wafer level chip-scale package, 8 bumps; 0.75 x 1.55 x 0.60 mm

20 April 2022

Package information

1. Package summary

Terminal position code	B (bottom)
Package type descriptive code	WLCSP8
Package type industry code	WLCSP8
Package style descriptive code	UC (uncased chip)
Package body material type	X (other)
Mounting method type	S (surface mount)
Number of package outline detail graphic references	0
Issue date	12-8-2020
Manufacturer package code	SOT8023
Footprint dimensions (mm)	1.85 x 1.05
Footprint area (mm²)	1.94

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	-	-	1.55	-	mm
E	package width	-	-	0.75	-	mm
A	seated height	0.5675	-	0.6	0.6325	mm
A ₂	package height	0.425	-	0.437	0.449	mm
e	nominal pitch	-	-	0.4	-	mm
n ₂	actual quantity of termination	-	-	8	-	

2. Package outline

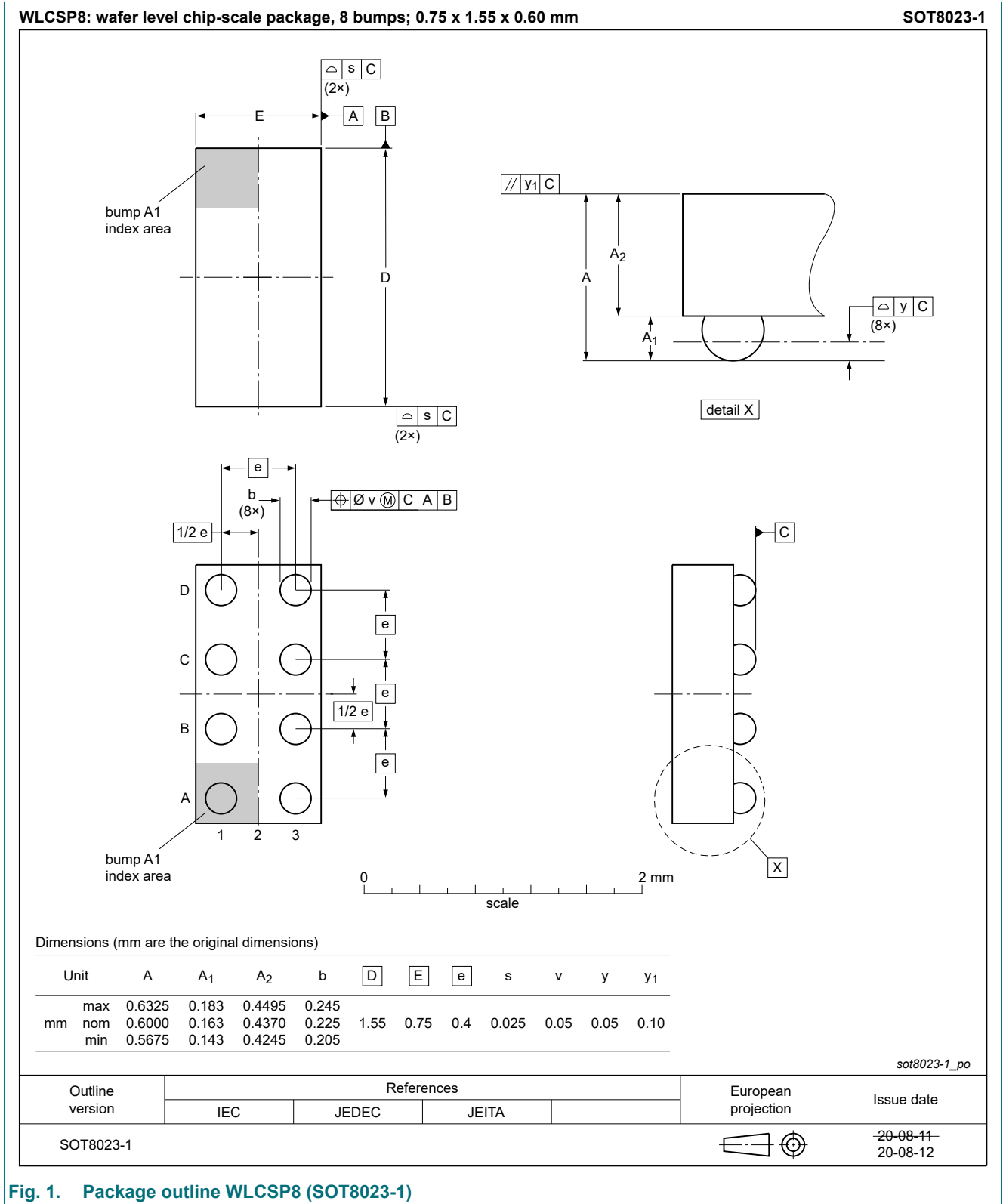


Fig. 1. Package outline WLCSP8 (SOT8023-1)

3. Soldering

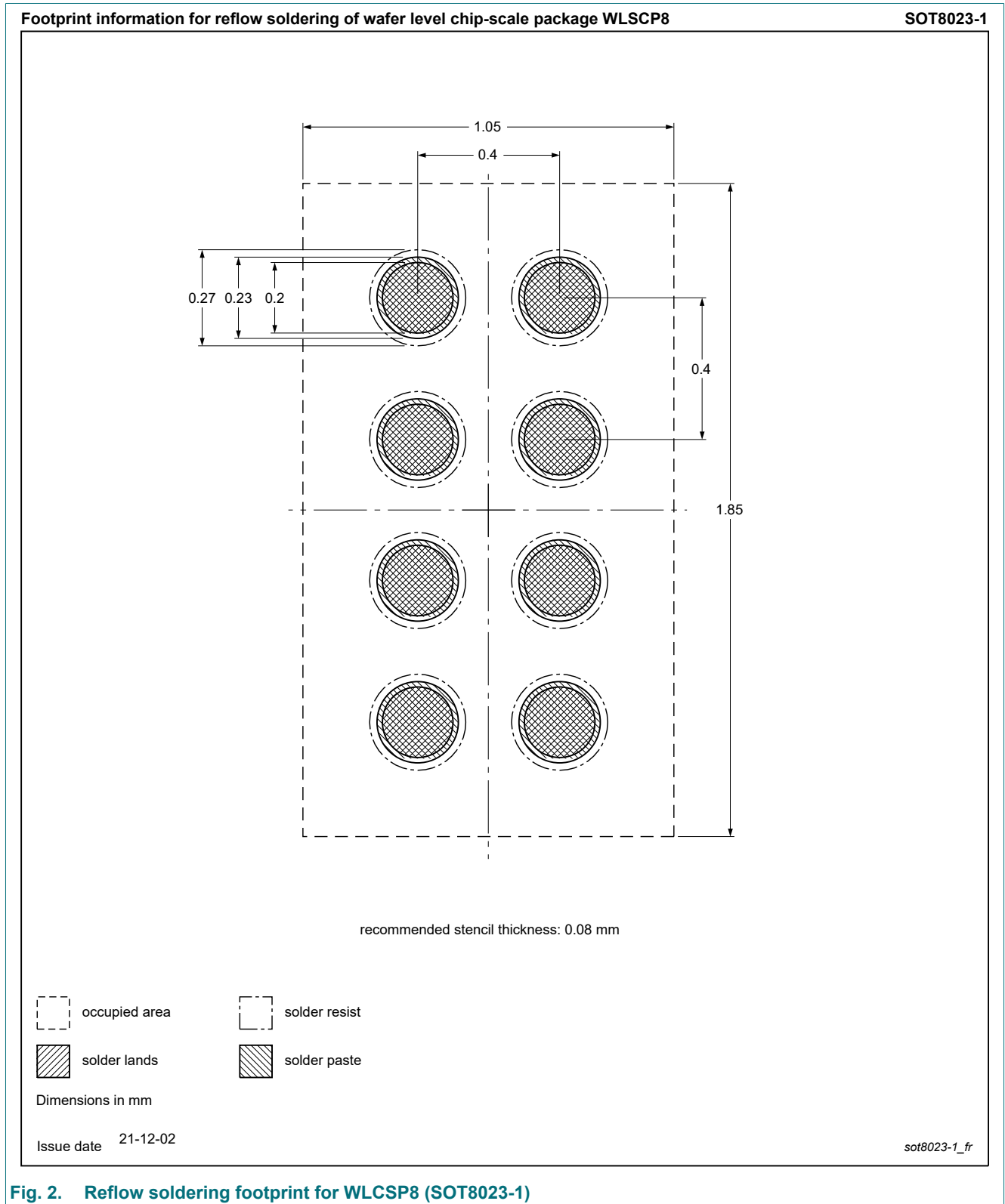


Fig. 2. Reflow soldering footprint for WLSCP8 (SOT8023-1)

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